

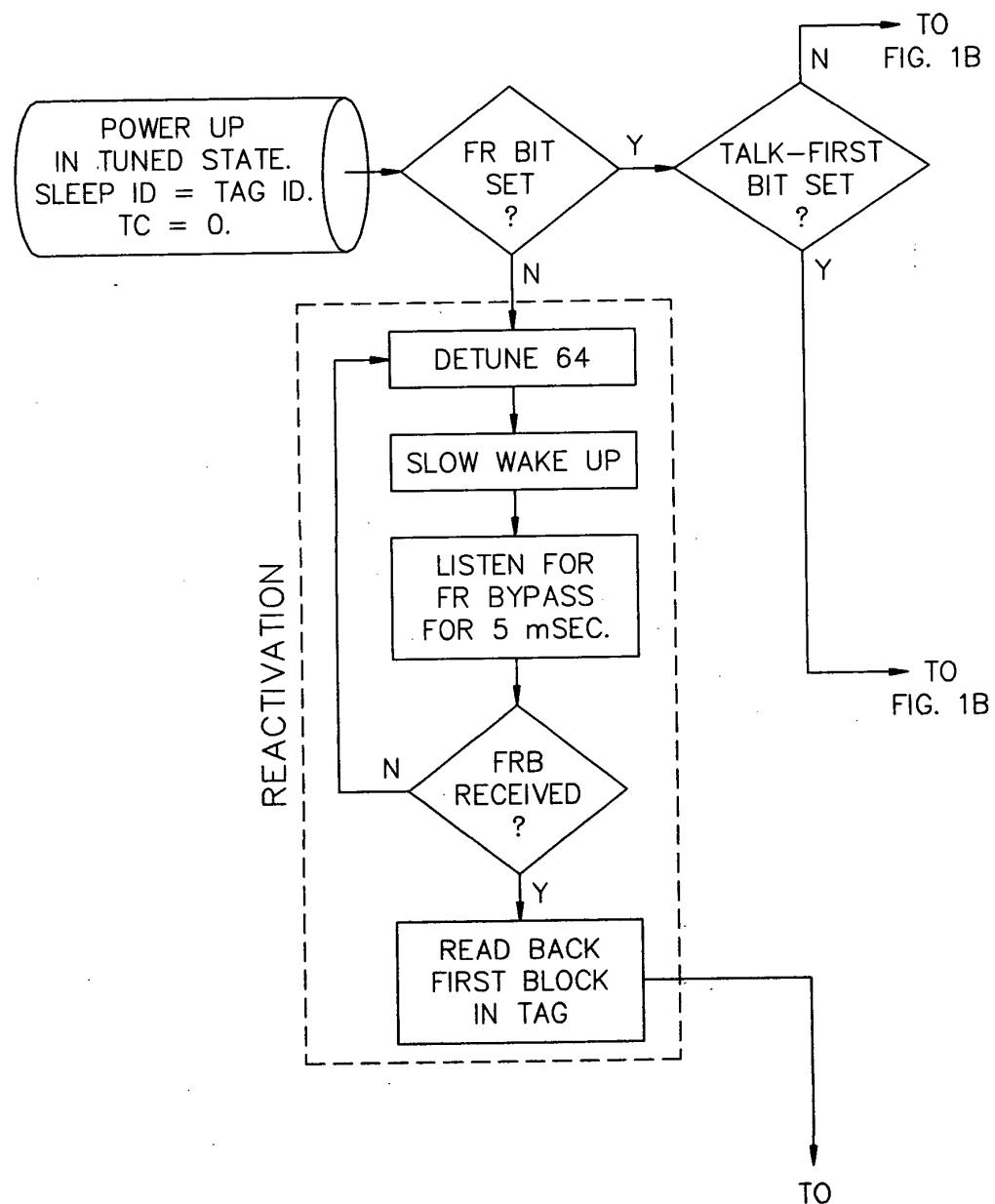
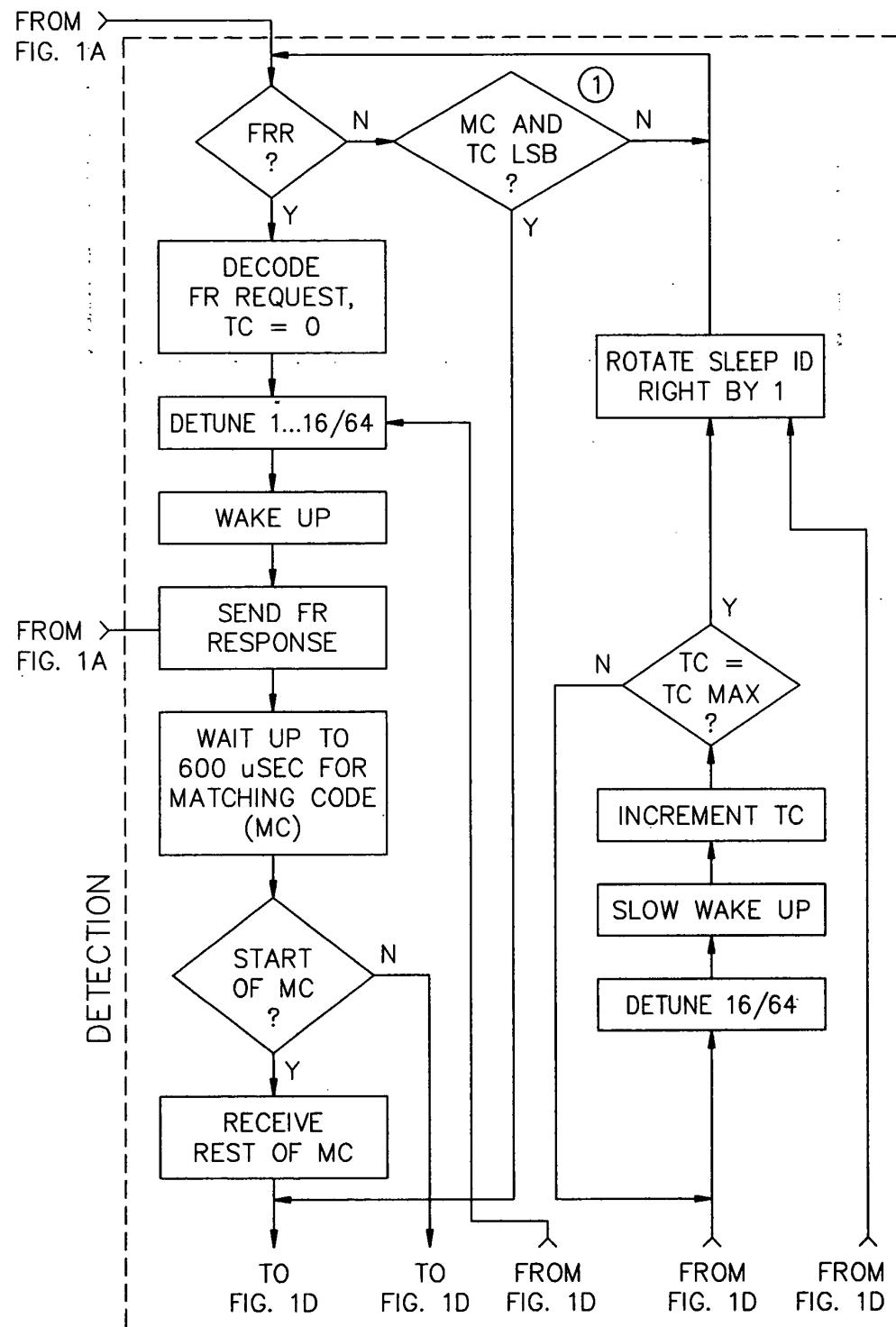
ANTI-COLLISION FLOWCHART

Fig. 1a

Fig. 1b



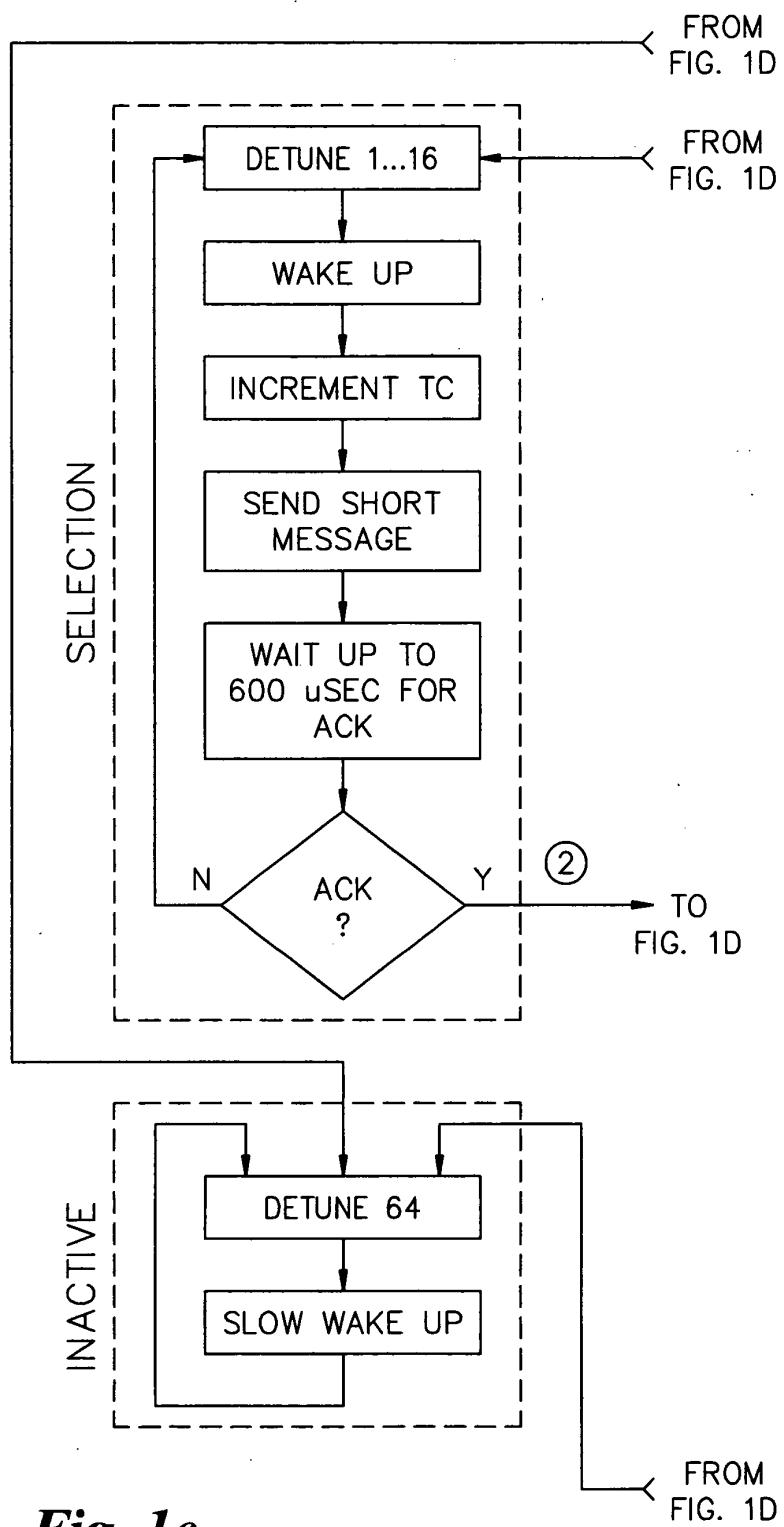
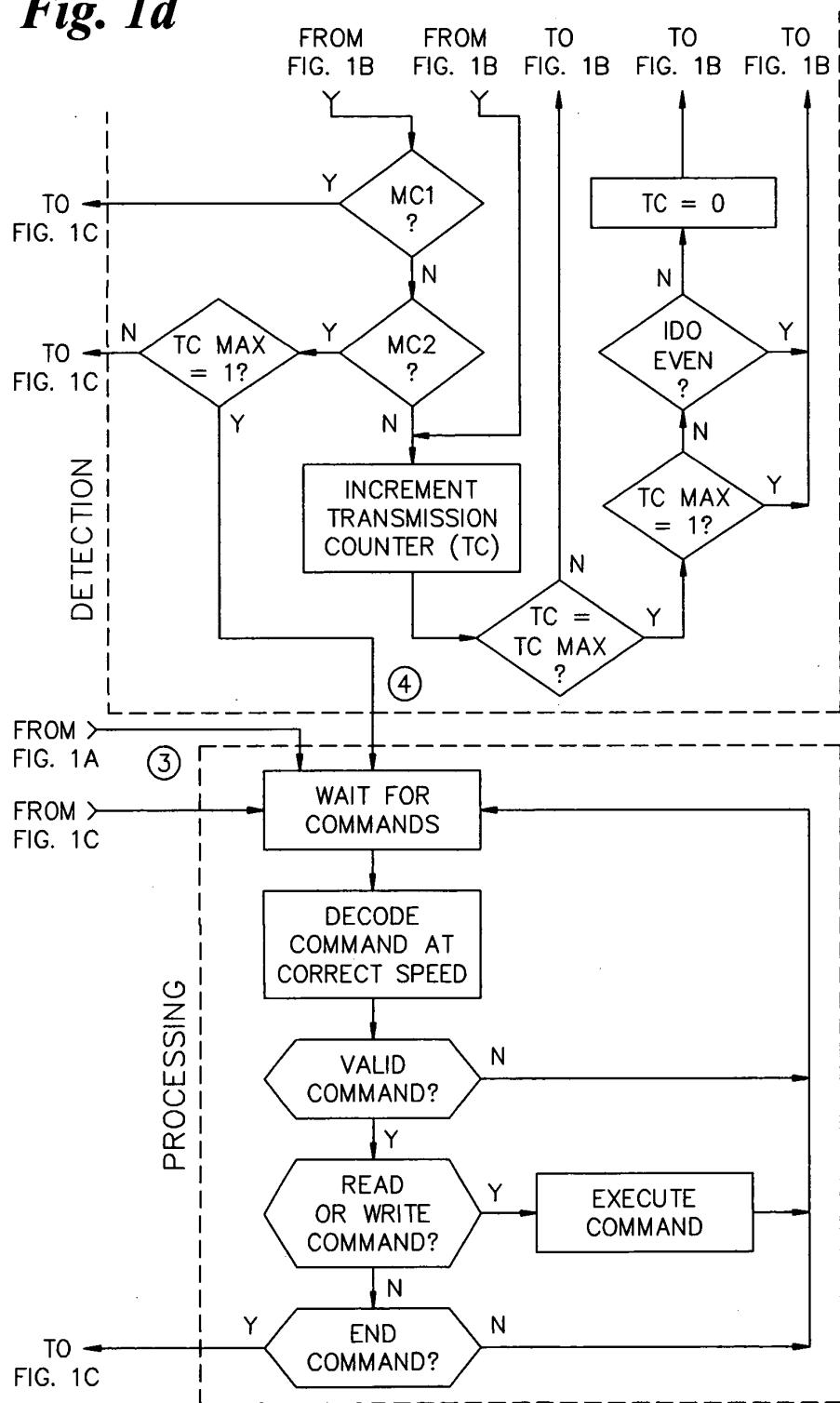


Fig. 1c

Fig. 1d



5/9

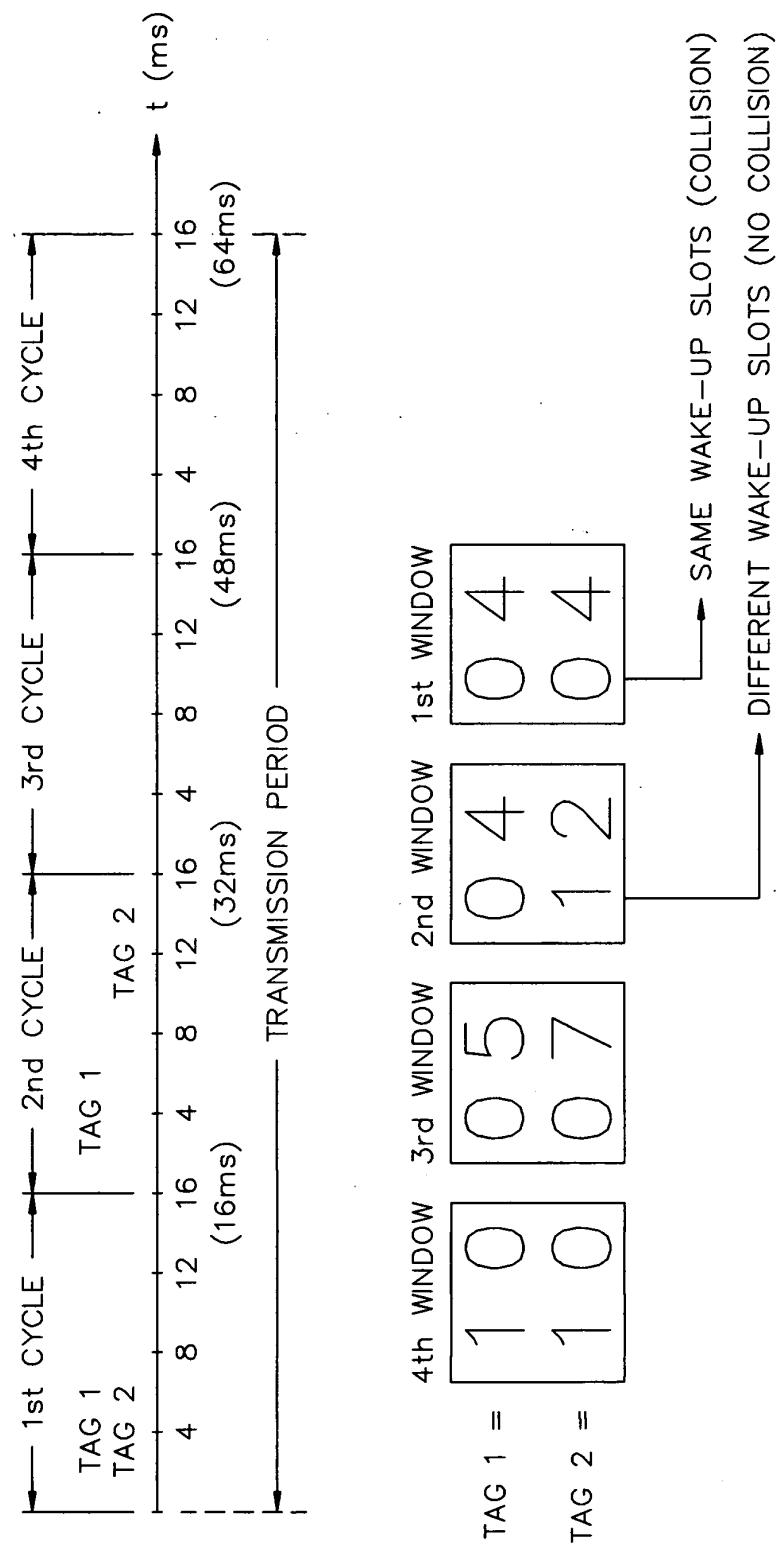
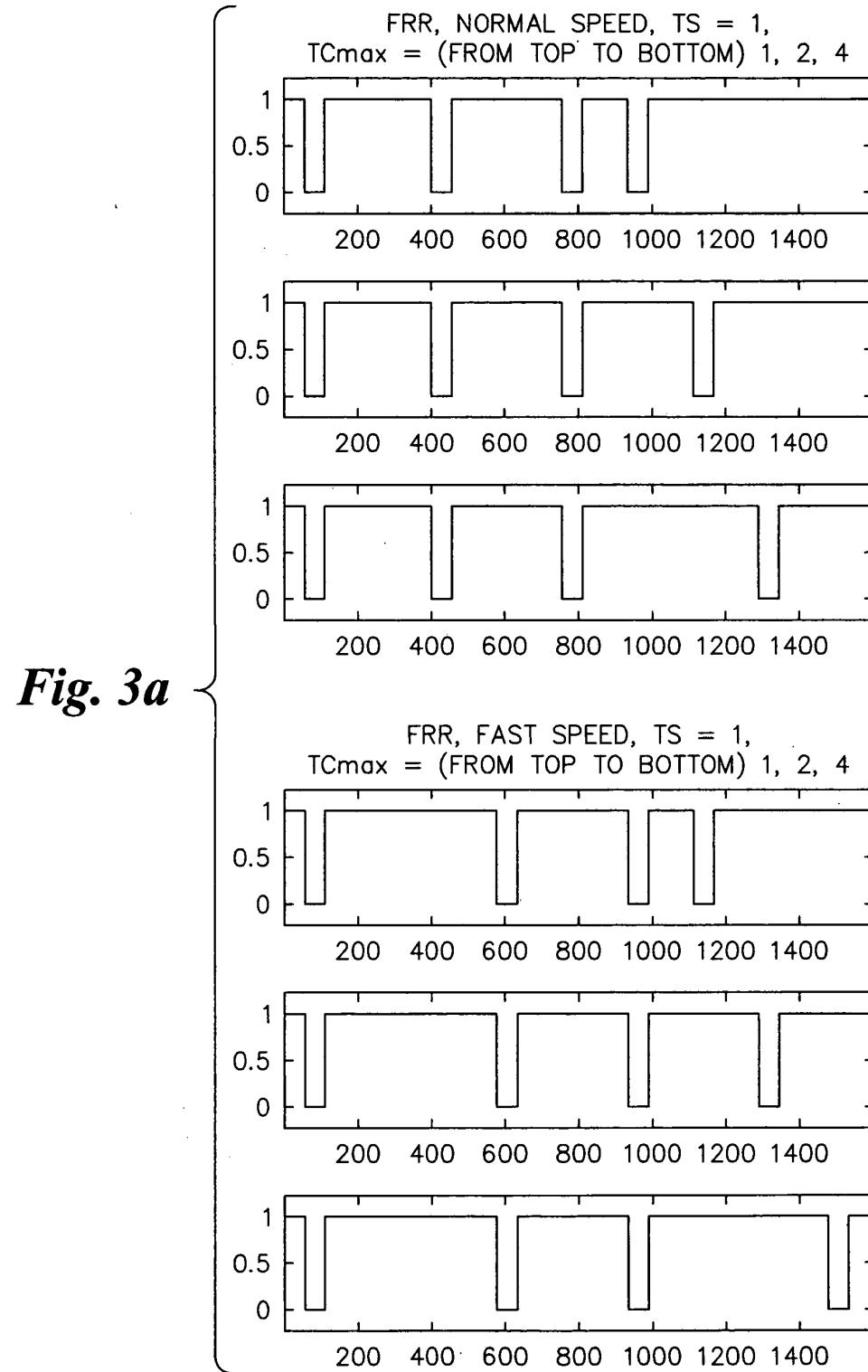
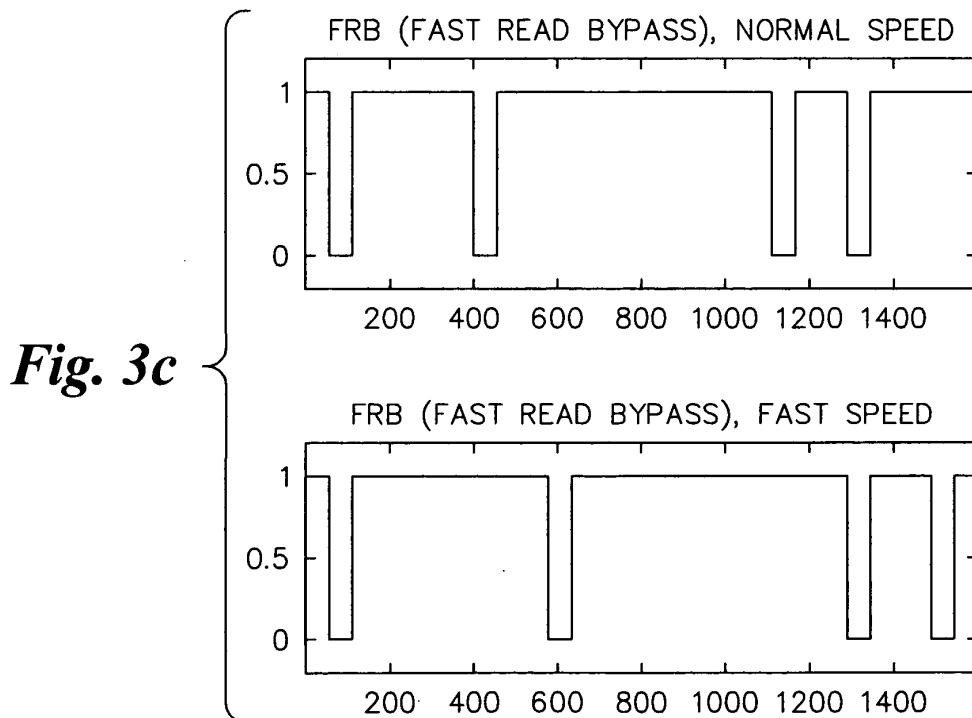
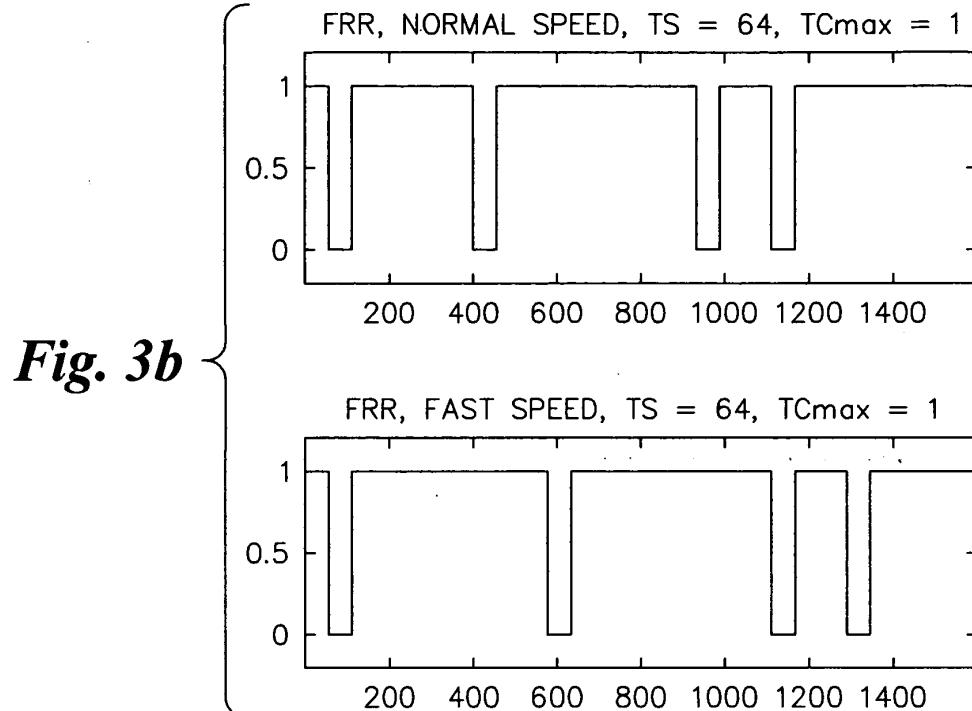


Fig. 2

6/9





"MATCH" CODE = TAG ID BIT RANGE A:B
[4(TC+1)+3]MODULO32:[4TC]MODULO32

Fig. 4

EXAMPLE: TAG ID = \$825FE1A0

TC	"MATCH"	ACK
0	\$A0	\$1
1	\$1A	\$E
2	\$E1	\$F
3	\$FE	\$5
4	\$5F	\$2
5	\$25	\$8
6	\$82	\$0
7	\$08	\$A

Fig. 5

ACKNOWLEDGE = TAG ID BIT RANGE A:B
[4(TC+2)+3]MODULO32:[4TC+8]MODULO32

Fig. 6

9/9

TIME SLOTS	WAKE-UP SLOT = TAG ID BIT RANGE A:B
16	[[4(TC+1)-1]MODULO32:[4TC]MODULO32] XOR TC LSB
64	[[4(TC+1)+1]MODULO32:[4TC]MODULO32] XOR TC LSB

Fig. 7

EXAMPLE: TAG ID = \$825FE1A0

TC	RELEVANT NUMBER	SLEEP TIME 16	SLEEP TIME 64	SLEEP TIME 16 SEMI-INV.	SLEEP TIME 16 SEMI-INV.	WAKE-UP SLOT
0	\$AO	b1010 0000	\$0	\$20	32	\$0
1	\$1A	b0001 1010	\$A	10	\$1A	26
2	\$E1	b1110 0001	\$1	1	\$21	33
3	\$FE	b1111 1110	\$E	14	\$3E	62
4	\$5F	b0101 1111	\$F	15	\$1F	31
5	\$25	b0010 0101	\$5	5	\$25	37
6	\$82	b1000 0010	\$2	2	\$02	2
7	\$08	b0000 1000	\$8	8	\$08	7

Fig. 8